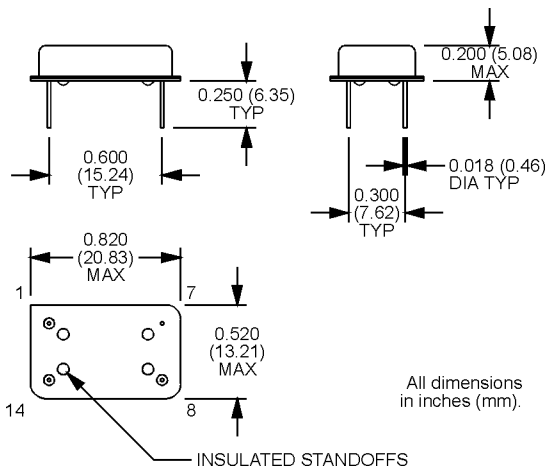


MA Series Advanced CMOS/TTL Compatible Oscillators

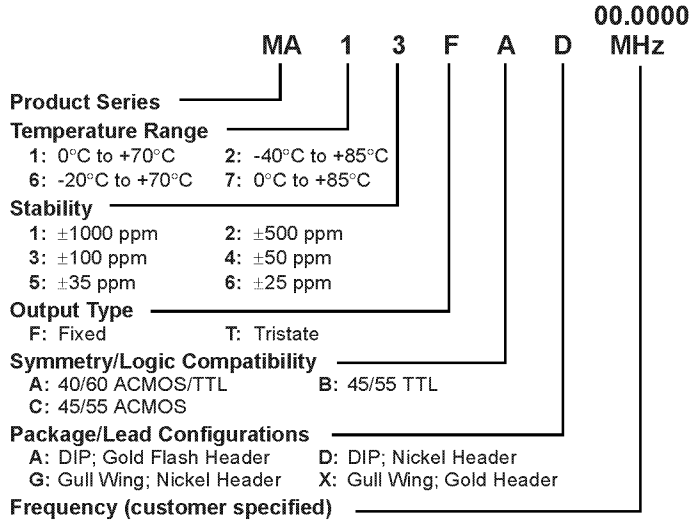


ACMOS and TTL Compatible High Frequency Oscillators



See page 135 for gull wing configuration.

Ordering Information



Pin Connections

PIN	FUNCTION
1	N/C or Tri-state
7	Circuit/Case Ground
8	Output
14	+Vdd

Tri-state Control Logic

Pin 1 high or floating: clock signal output
 Pin 1 low: output disables to high impedance

Available Stabilities vs. Temperature

T \ S	1	2	3	4	5	6
1	A	A	S	A	A	A
2	A	A	A	A	C	C
6	A	A	A	A	A	C
7	A	A	A	A	C	C

A = Available
 S = Standard
 C = Consult Factory

Electrical Specifications

Standard Operating Conditions • 0°C to +70°C; Vdd = 5.0 ±5% VDC							
	TTL Load			ACMOS Load			
PARAMETERS	MIN.	TYP.	MAX.	MIN.	TYP.	MAX.	UNITS
Frequency Range ¹	30.000		133.000	30.000		133.000	MHz
Output Load ²			50			50	Ω
Symmetry ³	40/60	45/55	60/40	40/60	45/55	40/60	%
Logic "0" Level			0.5			10% Vdd	V
Logic "1" Level	2.4V			90% Vdd			V
Rise/Fall Time ⁴			2			2	nS
Supply Current		70	85		70	95	mA

¹ Higher frequencies available by "special order". Contact factory.
² See load circuit diagram #6 on page 137.
³ Symmetry is measured at 1.4 V with TTL load, and at 50% Vdd with ACMOS load.
⁴ Rise/Fall times are measured between 0.5 V and 2.4 V with TTL load, and between 10% Vdd and 90% Vdd with ACMOS load.
 See page 136 for suggested soldering conditions.

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